

CMS-2M LANGUAGE

REFERENCE BOOKLET



REVISION B
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NAVSEA 0967-LP-598-2250 CMS-2M LANGUAGE

This booklet is a quick reference for programmers and operators using the machine-transferable CMS-2M compiler for the AN/UYK-20, AN/AYK-14, and AN/UYK-44 at Revision level 12.

The CMS-2M compiler is hosted on the following computers and operating systems.

Version	Computers	Operating Systems
01	Univac 1100 Series	EXEC 8 (Level 28 or later)
02	AN/UYK-20	Level 2
03	IBM 360 Series IBM 370 Series	SYSTEM/360 O.S. R21.8 OS/VS2 (SVS) R1.7 OS/VS2 (MVS) R3.7
04	CDC 6000 Series CYBER 70 Series CYBER 170 Series	KRONOS 2.1, NOS 1.0 (excluding NOS/BE 1.0)
06	DEC-10 KA10 Series DEC-10 KI10 Series DEC-10 KL10 Series	TOPS-10 Operating System (7.01)
07	AN/UYK-7	SHARE/7 Operating System (Level 4 or later)
09	Honeywell 6000 Series	GCOS Level F or higher
10	IBM 370 Series	VM/370-CMS Version 3
11	DEC VAX-11/780	VAX/VMS Operating System (Version 2.4)
12	CYBER 176 Series	TRW/TSS Operating System
13	CDC 6000 Series CDC 70 Series CDC 170 Series	SCOPE 3.4 NOS/BE 1.0
14	DECSYSTEM-20 Series	TOPS-20 Operating System (Monitor 4)

Reference Documents

User's Handbook for Support Software NAVSEA 0967-LP-598-2040, VOL. IV CMS-2M

User's Handbook for Support Software NAVSEA 0967-LP-598-2030, VOL. III Hardware

Computer Program Performance Specification for CMS-2M Compiler NAVSEA 0967-LP-598-2210

AN/AYK-14 Programmers Reference Manual (Preliminary) AN/AYK-14 Programmers Reference Card (Preliminary)

To obtain further information and to order additional copies of this booklet, please contact:

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CHARACTER SETS

	HARACTER	A	SCII	FIELD DATA	EBCDIC	DISPLAY	BCD
		OCT.	HEX.	OCT.	HEX.	OCT.	OCT.
	NUL	00	00		00		
	SOH	01	01		01		
	STX	02	02		02		
	ETX	03	03		03		
	EOT	04	04		37		
	ENQ	05	05		2D		
	ACK	06	06		2E		
	BEL	07	07		2F		
	BS	10	08		16		
	HT	11	09		05		
	LF	12	0A		25		
	VT	13	OB		OB		
	FF	14	OC OC		OC OC		
	CR	15	OD OD		OD OD		
	SO	16	0E		0E		
	SI	17	0F		0F		
	DLE	20	10		10		
	DC1	21	11		11		
	DC2	22	12		12		
	DC3	23	13		-		
	DC4	24	14		3C		
	NAK NAK	25	15		3D		
	SYN	26	16		32		
	ETB	27	17		26		
	CAN	30	18		18		
	EM	31	19		19		
	SUB	32	1A		3F		
	ESC	33	1B		27		
	FS	34	1C		22		
	GS	35	1D		_		
	RS	36	1E		35		
	US	37	1F		-		
	SP (Space)	40	20	05	40	55	20
1	(Exclamation)		21	55	5A	33	77
!	(Quotes)	41	22	33	7F		76
#	(Number)	42	23	03	7F 7B		13
\$		43	23	47	7B 5B	53	53
1 %	(Dollar)		25	52	6C	33	74
1	(Percent)	45 46	25	52	50		32
&	(Ampersand)		E MANAGE E	72	7D	64	57
	(Apostrophe)	47 50	27 28	51	4D	51	35
((Left Parenthesis)	50	28	21	40	21	30

CHARACTER SETS (continued)

UNALIZATEN GETO (GUNUNGGO)							
CHARACTER	AS	SCII	FIELD DATA	EBCDIC	DISPLAY	BCD	
	OCT.	HEX.	OCT.	HEX.	OCT.	OCT.	
) (Right Parenthesis)	51	29	40	5D	52	55	
* (Asterisk)	52	2A	50	5C	47	54	
+ (Plus)	53	2B	42	4F	45	60	
, (Comma)	54	20	56	6B	56	73	
- (Minus)	55	2D	41	60	46	52	
. (Period)	56	2E	75	4B	57	33	
/ (Slant)	57	2F	74	61	50	61	
0	60	30	60	F0	33	0	
1	61	31	61	F1	34	1	
2	62	32	62	F2	35	2	
3	63	33	63	F3	36	3	
4	64	34	64	F4	37	4	
5	65	35	65	F5	40	5	
6	66	36	66	F6	41	6	
7	67	37	67	F7	42	7	
8	70	38	70	F8	43	10	
9	71	39	71	F9	44	11	
: (Colon)	72	3A	53	7A	00	15	
; (Semicolon)	73	3B	73	5E	77	56	
< (Less than)	74	30	43	4C	72	36	
= (Equals)	75	3D	44	7E	54	75	
> (Greater than)	76	3E	45	6E	73	16	
? (Question Mark)	77	3F	54	6F	-	17	
@ (At)	100	40	00	7C	-	14	
A	101	41	06	C1	01	21	
В	102	42	07	C2	02	22	
C	103	43	10	C3	03	23	
D	104	44	11	C4	04	24	
E	105	45	12	C5	05	25	
F	106	46	13	C6	06	26	
G	107	47	14	C7	07	27	
Н	110	48	15	C8	10	30	
1	111	49	16	C9	11	31	
J	112	4A	17	D1	12	41	
K	113	4B	20	D2	13	42	
L	114	4C	21	D3	14	43	
M	115	4D	22	D4	15	44	
N	116	4E	23	D5	16	45	
0	117	4F	24	D6	17	46	
Р	120	50	25	D7	20	47	

CHARACTER SETS (continued)

CHARACTER	A	SCII	FIELD DATA	EBCDIC	DISPLAY	BCD
20.0	OCT.	HEX.	OCT.	HEX.	OCT.	OCT.
Q	121	51	26	D8	21	50
R	122	52	27	D9	22	51
S	123	53	30	E2	23	62
T	124	54	31	E3	24	63
U	125	55	32	E4	25	64
V	126	56	33	E5	26	65
W	127	57	34	E6	27	66
X	130	58	35	E7	30	67
Υ	131	59	36	E8	31	70
Z	132	5A	37	E9	32	71
[(Left	133	5B	01	AD	61	12
Bracket)	100	00		7.10		, i-
\ (Reverse Slant)	134	5C	57	E0	-	37
] (Right Bracket)	135	5D	02	BD	62	34
A (Circumflex)	136	5E	-	_	-	40
(Underline)	137	5F	-	6D	_	72
(Grave Accent)	140	60	-	79	-	-
a	141	61	-	81	_	_
b	142	62	_	82	_	_
c	143	63	_	83	_	_
d	144	64	_	84	_	
e	145	65		85	_	_
f	146	66	_	86	_	
g	147	67	_	87	_	_
h	150	68	_	88	_	_
i	151	69	-	89	_	_
	152	6A		91	_	_
k	153	6B	- 1	92	_	-
i	154	6C	_	93	_	_
m	155	6D	_	94	_	_
n	156	6E	- 1	95	_	
0	157	6F		96	_	
p	160	70	_	97	_	_
q	161	71	_	98	_	_
r	162	72		99	_	
S	163	73	_	A2	_	_
t	164	74		A3	_	_
u	165	75		A4	_	
v	166	76	_	A5	_	_
w	167	77		A6		_

CHARACTER SETS (continued)

CHARACTER	ASCII		FIELD DATA	EBCDIC	DISPLAY	BCD
	OCT.	HEX.	OCT.	HEX.	OCT.	OCT.
X	170	78		A7		-6
у	171	79	-	A8	-	-
Z	172	7A	-	A9	-	-
{ (Left Brace)	173	7B	-	8B	-	
(Vertical Line)	174	7C	-	-	-	-
} (Right Brace)	175	7D	-	9B	_	-
~ (Tilde)	176	7E	-	-	-	-
DEL	177	7F	-	07	-	-

NOTATION OF STATEMENTS AND OPERATIONS

Each description of a statement or an operation in this reference booklet uses a uniform system of notation to define the structure of the statement. This notation is not a part of CMS-2M, but is a standardized notation that may be used to describe the syntax (construction) of the CMS-2M language. It provides a brief but precise means of explaining the general patterns that the language permits. It does not describe the meaning of the statement or operations; it merely describes structure; that is, it indicates the order in which the operands must appear, the punctuation required, and the options allowed.

The following rules explain this standard notation:

 A word written in lowercase letters represents the type of entry to be made by the programmer. This word may be hyphenated.

name

denotes an entry of a name.

data-unit-name

denotes an entry of a data unit

 A word written in uppercase letters or special characters denotes an actual occurrence of that word or character in the language.

name EQUALS tag-expression

allows a symbolic name denoted by name to be associated with the value defined by a tag expression.

 Braces { } are used to denote a choice. The units from which a choice may be made are stacked vertically within the braces. At least one of the units within the braces must occur in the statement.

 $\left\{ \begin{array}{c} S \\ N \\ A \end{array} \right\}$

indicates that either S, N, or A must appear in the statement.

4) Square brackets [] are used to denote options. When one unit is enclosed in brackets, the unit may or may not appear. When more than one unit is enclosed in brackets, any one of the alternative units may or may not be chosen to appear. In either case, it is possible that no unit may appear. It is generally not possible that more than one unit will appear.

[name]

indicates that a name may appear in the statement format. However, this unit is not required.

5) The use of • • • denotes that the type of entry indicated by the word preceding • • may appear one or more times in succession, where each entry is delimited by the word preceding •. This does not imply that all entries should be identical. It does imply, however, that all entries should be the same type of entry indicated by the word preceding the three dots. Where there are two or more entries, they are separated by commas (.).

edata-unit-namee e e

indicates that one or more data unit names may occur in succession as entries, separated by commas. Thus, the following would be a legal entry: ALPHA, BETA, GAMMA.

6) A word written in lowercase letters and underlined represents a descriptive term that may be applied to a parameter. computer

UYK20 AYK14 indicates that this parameter identifies the target computer

When a descriptive term has been defined once with a list of alternatives, the alternatives are not listed in subsequent appearances of the underlined descriptive term. For example, the first time the term type is used, the possible alternatives for type are given. In subsequent references just the descriptive term type is used.

 Editorial Comment — For statements that are physically too long to be completed on one line, the lines following the first are indented to signify continuation. The dollar (\$) character signifies the end of a CMS-2M statement and is not part of the standard notation.

CMS-2M STATEMENTS

The statements within each of the sections for CMS-2M are given in alphabetical order by using the statement symbol.

STATEMENT FORMAT

CMS-2M source cards consist of a card identification field in columns 1 through 10 and a statement field in columns 11 through 80 as shown in the following:

CC	CC	CC	CC
1	10	11	80
CAF		STATEMENT\$STATM	MENT\$

The identification field may be used for program identification and sequence numbers and has no effect on program compilation.

The statement field has a free format. Each CMS-2M statement is terminated by a dollar (\$). There may be more than one statement on a card or a statement may require more than one card. A statement will continue in columns 11-80 of each card until a dollar is encountered. If a symbol or string of characters is to span two cards, the first part must end in column 80 of the first card and the second part must start in column 11 of the second card.

Names, compiler keywords, and constants must be separated from each other by a blank character or a delimiter. When a delimiter is used as a separator, blank characters are not necessary but may be used if desired. A blank character may not be used within a name, compiler keyword, constant, or between the name and the period character in a statement label.

An embedded comment may be used within a statement. An embedded comment consists of 2 consecutive apostrophes () followed by comment text and terminated by 2 consecutive apostrophes. The comment text may not contain a dollar. The embedded comment is replaced by a single blank character during statement processing.

BASIC DEFINITIONS

```
Boolean-expression
(Boolean-expression)
numeric-expression
                      EQ
                                 numeric-expression
                      NOT
                      GT
                      LTEQ
                      GTEQ
                      LT
Boolean-function-call
Boolean-data-unit
Boolean-constant
COMP Boolean-expression
Boolean-expression AND
                                 Boolean-expression
                     OR
character-constant
H (character-string)
data-unit
name
name (name)
name (enumeric-expressione e e)
name (enumeric-expressione e. name)
expression
Boolean-expression
literal-expression
numeric-expression.
labels
name
labels name
literal-expression
literal-data-unit
literal-function-call
character-constant
numeric-constant
U (octal-integer [.[octal-integer]])
0 (.octal-integer
decimal-integer [.[decimal-integer]] [E
                                           decimal-integer]
.decimal-integer [E
                           decimal-integer]
X (hexadecimal number [.[hexadecimal number]])
X (.hexadecimal number)
numeric-expression
(numeric-expression)
numeric-function-call
numeric-data-unit
numeric-tag
        \ numeric-expression
                           numeric-expression
numeric-expression
```

```
numeric-tag
         numeric-constant
PROGRAM STRUCTURE STATEMENTS
              COMMENT
                             comment-text
                             character-string
                             ((EJECT
                             ((SKIPn
((LINE*
              CSWITCH name $
              END-CSWITCH name $
              SYSTEM $
    name
              END-SYSTEM name $
              TERMINATE $
HEADER DECLARATIVE STATEMENTS
    CMODE
              conversion-mode
              SINGLE
              DOUBLE
              FLOAT
              QUAD
    CSWITCH
              ecswitch-iteme e e $
              cswitch-item
    where:
              DELETE
              EQUALS tag-expression $
    name
              HEAD $
    name
              END-HEAD name $
              MEANS character-string $
    name
    OPTIONS
                             [eoptione e e] $
              computer
              UYK20
              AYK14
              AYK14IOP
              LUYK44
    where:
              option
              SOURCE
              ASM [(INLINE)]
              HEX
              REF
                                       (level)
              PSTRUCT
                                       W
              ERROR
              MATHPAC (EAU)
              FLOAT
              TAPE [(character-string)]
              LINE (lines-per-page)
              DCREG
              GRMCHK
              OBJECT (DEBUG)
```

SYS-INDEX •register-number name• • • \$

SYSTEM DATA DECL CMS-2 \$	ARATIVE STATEMENTS	[(EXTREF)] (EXTDEF)	TABLE name A
DIRECT \$ FIELD name	type [starting-word starting-bit]		MEDIUM DENSE (type)
	[P •preset-item• • •] \$	(EXTREF)	TABLE name { V } packing [INDIRECT] number-of-items \$
	where: (type F B	2 (2	END-TABLE name \$
	A number-of-bits { S	(EXTREF) (EXTDEF)	VRBL { variable-list name (●name● ●) } type
	where: preset-item		P preset-tag numeric-tag character-constant CORAD (data unit) \$
	repeat-count (character-constant) repeat-count (CORAD (data unit))	SYSTEM PROC	EDURE STATEMENTS
(EXTREF)	FUNCTION function-name (oformal-input-parametero • •) type	[(EXTDEF)]	FUNCTION function-name (oformal-input-parametero o o) type \$
	\$		END-FUNCTION function-name \$
	where $\left\{\frac{\text{formal-input-parameter}}{\text{name}}\right\}$	[(LOCREF)]	FUNCTION function-name (•formal-input-parameter• • •) type \$
	CORAD (name)	[loc-dd-name]	LOC-DD access \$
[(EXTREF)] (EXTDEF)]	ITEM-AREA •name• • • \$		L W J
[(EXTREF)]	LIKE-TABLE name [number-of-items] \$	((EVIDEE))	END-LOC-DD [loc-dd-name] \$
L (EXTDEF)	DROCEDURE	[(EXTDEF)]	PROCEDURE procedure-name [INPUToformal-input-parameter • •] [OUTPUT oname • •] \$
(EXTREF)	PROCEDURE procedure–name [INPUT●formal–input–parameter● ● ●		END-PROC procedure-name \$
[(EVIDEE)]	[ÖUTPUT ●name● ● ●] \$	(LOCREF)	PROCEDURE procedure-name [INPUT•formal-input-
(EXTREF) (EXTDEF)	P-SWITCH p-switch-name [INPUT•formal-input- parameter• • •] [OUTPUT • name• • •] \$	(LOUNLI)	parameter • • •] [OUTPUT •name• • •]
	procedure-list		SWITCH switch-name \$
	procedure-name \$ procedure-list procedure-name \$		label-list label \$
	END-SWITCH p-switch-name \$		label-list label \$
sys-dd-name	SYS-DD \$		END-SWITCH switch-name \$
	END-SYS-DD sys-dd-name \$	sys-proc-name	SYS-PROC \$
			END-SYS-PROC sys-proc-name \$

PROCEDURE BODY STATEMENTS

[begin-labels] BEGIN [ofor-value o o] \$ for-value where: constant-numeric-value character-constant [labels] END block-label begin-label for-label vary-label ELSE simple-statement direct-code-block vary-block begin-block comment-statement cswitch-statement set-statement procedure-call-statement p-index-statement stop-statement return-statement resume-statement goto-statement for-block [for-labels] FOR expression ELSE simple-statement \$ [labels] GOTO label \$ [labels] GOTO switch-name numeric-expression [INVALID label] \$ [labels] IF Boolean-expression THEN simple-statement \$ LOC-INDEX enamee . . \$ procedure-name [INPUT •[expression]• • •] [labels] OUTPUT edata-unite e el \$ [labels] p-switch-name USING numeric-expression [INVALID label] [INPUT elexpression] • • OUTPUT edata-unite e e 1 \$ [labels] RESUME vary-label \$ [labels] RETURN [(expression)] \$ [labels] SET ereceptaclee e TO expression \$ where: receptacle data-unit CORAD (name) CHAR (data-unit, starting-character, count) BIT (data-unit, starting-bit, count) [labels] STOP KEY 1 label \$ KEY 2 [vary-labels] VARY [data unit [FROM numeric-expression] THRU numeric-expression [BY [-] numeric-expression]
[WHILE Boolean-expression] [UNTIL Boolean-expression] \$

FUNCTION CALLS

function-name (expressione e e) ABS (numeric-expression) ANDF (numeric-expression, numeric-expression) BIT (data-unit, starting-bit, count) CHAR (data-unit, starting-character, count) CNT (numeric-expression) CONF (type, numeric-expression) CORAD (data-unit) ORF (numeric-expression, numeric-expression) REM (numeric-expression) SCALF (scale factor, numeric-expression) SHIFTAL (numeric-expression, count) SHIFTAR (numeric-expression, count) SHIFTCL (numeric-expression, count) SHIFTCR (numeric-expression, count) SHIFTLL (numeric-expression, count) SHIFTLR (numeric-expression, count) TDEF (type, numeric-expression) XORF (numeric-expression, numeric-expression)

The following calls are only available under the UYK20 or UYK44 and MATHPAC option.

MATHPAC PROCEDURE CALLS

 mathpac-procedure

 VECTOR

 VECTORP

 VECTORH

 VECTORHP

 ROTATE

 ROTATEH

 ROTATEH

 ROTATEH

 ROTATEH

MATHPAC In-Line Function Calls

BAMS (numeric-expression)
HLN (numeric-expression)
HLN (numeric-expression)
HLN (numeric-expression)
HLN (numeric-expression)
HLN (numeric-expression)
HLN (numeric-expression)
LN (numeric-expression)

The following calls are available under the AYK14 or UYK44 and MATHPAC (EAU) option:

MATHPAC (EAU) In-Line Function Calls

SIN (numeric-expression)
COS (numeric-expression)
TAN (numeric-expression)
ASIN (numeric-expression)
ACOS (numeric-expression)
ATAN (numeric-expression)
EXP (numeric-expression)
EXP (numeric-expression)
SQRT (numeric-expression)

DIRECT CODE STATEMENTS

Direct Code Statement Format

The format of direct code statements is consistent with high level in that columns 1 through 10 is the card identification field, which is ignored by the compiler.

The direct code format consists of four fields separated by at least one blank as follows:

Label operation operand .comment

The label must always start in column 11. Labels having an x subscript below may externally define a symbol by suffixing it with an asterisk (*). The operation may be a machine-instruction mnemonic or a direct code directive. The operand field may contain subfields separated by commas as specified for the operation code. The operand field may contain the dollar (\$) to signify the current value of the location counter. A period (.) followed by a blank signifies the end of the statement and the remainder of the line may contain a comment.

Basic Direct Code Definitions

character-constant

direct-code-constant
single-word-integer constant
double-word-integer-constant
character-constant

double-word-integer-constant
decimal-integer D
hexadecimal-number D

\$\phi\$ octal-integer D

instruction-expression
name [± single-word-integer-constant]
\$ [± single-word-integer-constant]
[±] single-word-integer-constant
literal

{ literal (direct-code-constant)

single-word-integer-constant decimal-integer hexadecimal-number \$\phi\$ octal-integer

Direct Code Directives

[label_X] BSS instruction-expression

[label] D0 instruction-expression, symbolic-line

EVEN

form-label FORM einstruction-expressione . .

[labely] form-label einstruction-expressione e

ODD

ORIG instruction-expression

REORIG

[label_X] RES instruction-expression

The following directives are only available with the UYK44 option.

[labelx] ABS relocatable name ± constant

[labelx] PAGE mp, relocatable name ± constant

LINKAGE AND PARAMETER PASSING

All procedures and functions are called using the JLR R4, NAME instruction (RK format) where NAME is the called procedure or function name. One and two word typed parameters are passed in registers as described below. Other parameters are passed directly in memory by copying the actual parameters to the formal parameters.

Input parameters are passed in registers R5, R3–R0, R15–R12 until all registers are used. Output parameters are passed in registers R5–R0 and R15–R12 until all registers are used. The parameters are assigned to registers from left to right as they appear in the procedure or function call. Registers are assigned from the list starting at R5 and working backwards through R12. Two word parameters are assigned to the first available even-odd register pair. One word parameters are assigned to the first available (including registers skipped to assign an even-odd register pair). When all registers have been used, remaining parameters are passed directly in memory.

REGISTER SAVING CONVENTIONS

The calling program is responsible for saving and restoring the contents of any registers in the group R0-R5 or R12-R15 that contain data that must be preserved across a procedure or function call. The calling program is also responsible for loading and storing the contents of R0-R5 and R12-R15 when used for parameter passing before and after a procedure or function call.

The called program is responsible for saving and restoring the contents of R6-R11 when used as compiler work registers or as local indices or when direct code is used in the called procedure or function and the DCREG option is not used. If the DCREG option is used, registers in the group R6-R11 will be saved and restored only if used as compiler work registers or as local indices. Registers declared as system indices will never be saved and restored.

ADDRESS COUNTER USAGE

All data in a SYS-DD is generated under Address Counter 1. The name of the address counter will be that on the SYS-DD statement.

All data in a procedure block, function block, and LOC-DD in a SYS-PROC is generated under Address Counter 0. All temporary storage and constants generated by the compiler before a LOC-DD W or LOC-DD R statement has been encountered will be under Address Counter 0. The name of the address counter will be that on the SYS-PROC statement.

All data in a LOC-DD W is generated under Address Counter 3. All temporary storage (read-write access) generated by the compiler after the first LOC-DD W statement has been encountered will be generated under Address Counter 3. The name of the address counter will be that on the first LOC-DD W statement.

All data in a LOC-DD R is generated under Address Counter 5. All constants (read-only) generated by the compiler after the first LOC-DD R statement has been encountered will be under Address Counter 5. The name of the address counter will be that on the first LOC-DD R statement.

LOGICAL UNIT USAGE

FORTRAN logical units used by the CMS-2M compiler are:

Unit	Usage
5	CMS-2M Source Input
10	Printer Listings Output Compiler Interphase Scratch File
11	Compiler Interphase Scratch File
12	Relocatable Object Output (UYK-20 Only)
13 15	Compiler Interphase Scratch File (INLINE option only) Relocatable Object Output (TAPE option only)
20,others	INCLUDE Control Card Source Input
INDX1	Scratch Unit (Versions 3 and 10 only)
INDX2	Scratch Unit (Versions 3 and 10 only)

INCLUDE CONTROL CARD

The INCLUDE control card inserts additional CMS-2M source into the source input during compilation of a CMS-2M program. It is only available on MTASS-hosted versions of CMS-2M and is not available on the UYK-20-hosted version. The INCLUDE control card format is:

= INCLUDE [decimal-constant] (name)

where: The = character must be in column 11.

The decimal-constant is a one or two digit logical unit number (default value is 20 if not present).

The name is a 1-8 character name of the member or element of the specified file. There can be no space between the decimal number and the left parenthesis.

CMS-2M RESERVED WORDS

Certain symbols that are language keywords in CMS-2M are reserved words and may not be used as names to identify entities in a CMS-2M program. If any of these reserved words are used in a CMS-2M source program, a fatal error message will be given. Additional symbols that are reserved words in CMS-2Y (on the AN/UYK-7 computer) but not in CMS-2M will be allowed as names in CMS-2M but a warning error message will be given for compatibility purposes.

CMS-2M RESERVED WORDS

	A		END	**	ISQRT		SHIFTAR
	ABS		EQ		KEY1		SHIFTCL
****	ACOS		EQUALS		KEY2		SHIFTCR
****	ALOG	****	EXP	*	LIBS		SHIFTLL
	AND		EXTDEF	**	LN		SHIFTLR
	ANDF		EXTREF		LOCREF	****	SIN
****	ASIN		F		LT	****	SQRT
****	ATAN	***	FADD		LTEQ		STOP
	В	***	FDIV		MEANS		SWITCH
**	BAMS		FIELD		MEDIUM		SYSTEM
	BEGIN	***	FMUL		NONE		TABLE
	BIT		FOR		NOT	****	TAN
	BY		FROM		0		TDEF
	CHAR	***	FSUB		OPTIONS		THEN
	CMODE		FUNCTION		OR		THRU
	CNT		GOTO		ORF		TO
	COMMENT		GT		OUTPUT		U
	COMP		GTEQ		P		UNTIL
	COMPF		Н		REM		USING
	CONF		HEAD		RESUME		٧
	CORAD	**	HLN		RETURN		VARY
*	CORRECT		1	**	ROTATE	**	VECTOR
****	COS	**	ICOS	**	ROTATEH	**	VECTORH
	CSWITCH	**	IEXP	**	ROTATEHP	**	VECTORHP
	DELETE		IF	**	ROTATEP	**	VECTORP
	DENSE		INDIRECT		S		VRBL
*	DEP		INPUT		SCALF		WHILE
	DIRECT		INVALID		SET		X
	ELSE	**	ISIN		SHIFTAL		XORF

^{*} Reserved for compatibility with CMS-2Y.

^{**} Reserved only if MATHPAC option present.

^{***} Reserved only if MATHPAC option is not present and FLOAT conversion mode is present.

^{****} Reserved only if MATHPAC option with EAU is used with AYK14 option.

ADDITIONAL RESERVED WORDS FROM CMS-2Y ON AN/UYK-7

(Allowed as names in CMS-2M but warning error is given for compatibility)

See High High High			
ALG	ENDFILE	OCM	REGS
BASE	EVENP	ODDP	SAVING
CAT	EXCHANGE	OPEN	SHIFT
CHECKID	EXEC	OVERFLOW	SNAP
CIRC	EXIT	OVERLAY	SPILL
CLOSE	FIL	PACK	SWAP
D	FILE	POS	TRACE
DATA	FIND	PRINT	VALID
DATAPOOL	FORMAT	PTRACE	VARYING
DEBUG	INTO	PUNCH	WITH
DECODE	LENGTH	RANGE	WITHIN
DEFID	LOG	READ	XOR
DISPLAY	MODE		
ENCODE	NITEMS		

RUN-TIME LIBRARY ROUTINES

	RUN-TIME LIBRARY ROUTINES						
ROUTINE DESCRIPTION	INPUT	OUTPUT	LINKING CONVENTION				
CFLOA\$ Converts double-length fixed-point number to floating- point format	R1 = Scaling factor of input R2,R3 = Fixed-point number	of con	JLR R4,CFLOA\$				
CFIX\$ Converts floating- point-number to double- length fixed- point format	R1 = Scaling factor of output R2,R3 = Floating- point number	R2,R3 = Fixed- point number	JLR R4,CFIX\$				
FLTCM\$ Compares two floating-point numbers and sets condition code	R1 = Address of floating-point number R2,R3 = Number being compared	Condition code set in SR1	JLR R4,FLTCM\$				
FADD Adds two floating-point numbers	R1 = Address of Addend R2,R3 = Floating- point number	R2,R3 = Sum	JLR R4,FADD				
FSUB Subtracts two floating-point numbers	R1 = Address of subtrahend R2,R3 = Minuend	R2,R3 = Difference	JLR R4,FSUB				
FMUL Multiplies two floating-point numbers	R1 = Address of multiplier R2,R3 = Multi- plicand	R2,R3 = Product	JLR R4,FMUL				
FDIV Divides two floating-point numbers	R1 = Address of Divisor R2,R3 = Dividend	R2,R3 = quotient					
The following ro	utines may be used on availab		HPAC II option				
MATHFL\$ Converts double-length fixed-point number to floating-point format	R1 = Scaling factor of input R2,R3 = Fixed-point number	R2,R3 = Floating- point	JLR R4,MATHFL\$				

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RUN-TIME LIBRARY ROUTINES (continued)

ROUTINE DESCRIPTION	INPUT	OUTPUT	LINKING CONVENTION
MATHFX\$ Converts floating-point number to double-length fixed-point format	R1 = Scaling factor of output R2,R3 = Floating- point number	R2,R3 = Fixed- point number	JLR R4,MATHFX\$

Note: These library routines are not reentrant. They do not use R12 and R14 for indexing. FADD, FSUB, FMUL, and FDIV are called only if the MATHPAC option is not present and the target machine is the UYK-20.

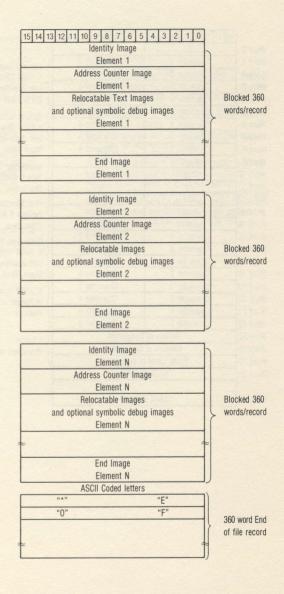


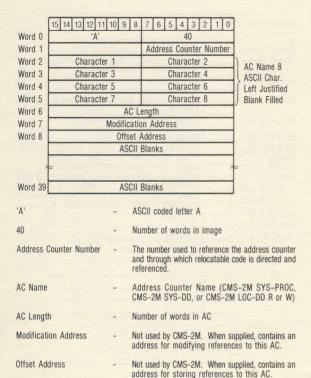
Figure 1. Sequential Object File Format (MTASS Hosts Only)

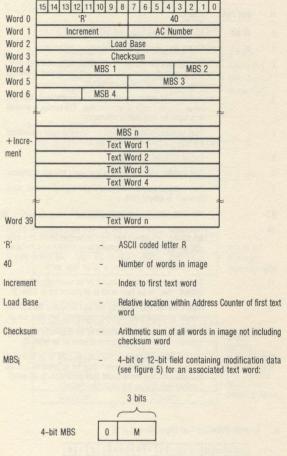
	15 14 13 12 11 10 9 8	7 6 5 4 3 2 1 0						
Word 0	T'	40						
Word 1	Character 1	Character 2	Element Name 8 ASCII Chars					
Word 2	Character 3	Character 4						
Word 3	Character 5	Character 6	Left Justified					
Word 4	Character 7	Character 8	Blank Filled					
Word 5	Character 1	Character 2	System Name					
Word 6	Character 3	Character 4	8 ASCII Chars.					
Word 7	Character 5	Character 6	Left Justified					
Word 8	Character 7	Character 8	Blank Filled					
Word 9	Month	Month	Month, Day					
Word 10	Day	Day	Year 6 ASCII					
Word 11	Year	Year	Characters					
Word 12	Hour	Hour	Hour, Minutes					
Word 13	Minutes	Minutes	Seconds 6 ASC					
Word 14	Seconds	Seconds	Characters					
Word 15	Character 1	Character 2	Component Nan					
Word 16	Character 3	Character 4	8 ASCII Chars					
Word 17	Character 5	Character 6	Left Justified					
Word 18	Character 7	Character 8	Blank Filled					
Word 19	Character 1	Character 2	8 ASCII Chars					
Word 20	Character 3	Character 4	of Target					
Word 21	Character 5	acter 5 Character 6						
Word 22	Character 7	Character 8	Machine					
Word 23	Version of	Component	2 ASCII					
Word 24	Revision of	Component	Characters each					
Word 25	Patch Level of	of Component) onaractors sacr					
Word 26	ASCII B	BLANKS						
Word 39	ASCII B	BLANKS	=					

Figure 2. Identity Image Format

Description of 'I' Image:

· · ·	_	ASCII coded letter I.
40	_	Number of words in image
Element Name		
Element Name	-	Name of element. A CMS-2M SYS-PROC or SYS-DD
		statement creates the element name
System Name	-	Name of System. A CMS-2M SYSTEM statement creates
		the system name
Word 9 - 11		Date of Compilation in numeric characters (For example
Word 5 - 11	100	
		4 July 1978 is 070478)
Word 12 - 14	-	Time of Compilation in numeric characters (For example,
		21:15 and 30 seconds is 211530)
Word 15 - 18	-	MTASS component (CMS2M)
Word 19 - 22	-	Target Machine (UYK20, AYK14, AYK14I0P, UYK-44)
Word 23	-	Version of Component in ASCII (Host Machine)
Word 24	-	Revision of Component in ASCII (Change Level)
Word 25	-	Patch Level of Component in ASCII (Priority 1 STR fixes)
		Two ASCII characters. Left character is the version
		independent patch level (0-9,A-Z). Right character is
111 1 00 00		the version dependent patch level (blank, A-Z).
Word 26 - 39	-	ASCII Blanks





4-bit MBS 0 M 5 bits 6 bits
12-bit MBS 1 M AC number

Figure 3. Address Counter Image Format

Figure 4. Relocatable Text Image Format

1 16-bit Modify text word by base load value. 2 Text contains externally defined relocatable symbol and value. The given text value is relocated and the symbol is entered in a table for succeeding references. 3 Text contains externally defined nonrelocatable symbol and value. The text value is not relocated. The symbol is entered in a table for succeeding references. 4 Text contains externally defined nonrelocatable symbol and value. The text value is not relocated. The symbol is entered in a table for succeeding references. 5 End of MBS for this image. 6 Text contains offset followed by an externally referenced symbol. 7 Text contains a relative address to allocate forward references. 8,9 Not used, reserved for future use. 10 Text contains two words, the first text word is logically shifted left 12 bits. The second text word is modified by the load base plus the address counter physical base address and the result is logically shifted 10 bits to the right. The logical sum of the shifted values produces a 16 bit load word. 11 Text contains two words followed by an externally referenced symbol. One 16 bit load word is produced as in MBS-10. 12 Modify the two text words by the load base plus the address counter physical base address to produce two load words. 13 Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words. External text entries are described below:	M Mod	Field Action
Text contains externally defined relocatable symbol and value. The given text value is relocated and the symbol is entered in a table for succeeding references. Text contains externally defined nonrelocatable symbol and value. The text value is not relocated. The symbol is entered in a table for succeeding references. Text contains offset followed by an externally referenced symbol. End of MBS for this image. Text contains a relative address to allocate forward references. Text contains partial word preset and logical product mask to insert in proper field. Not used, reserved for future use. Text contains two words, the first text word is logically shifted left 12 bits. The second text word is modified by the load base plus the address counter physical base address and the result is logically shifted 10 bits to the right. The logical sum of the shifted values produces a 16 bit load word. Text contains two words followed by an externally referenced symbol. One 16 bit load word is produced as in MBS-10. Modify the two text words by the load base plus the address counter physical base address to produce two load words. Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	0 16-bi	t No modification. Load text value given.
The given text value is relocated and the symbol is entered in a table for succeeding references. Text contains externally defined nonrelocatable symbol and value. The text value is not relocated. The symbol is entered in a table for succeeding references. Text contains offset followed by an externally referenced symbol. End of MBS for this image. Text contains a relative address to allocate forward references. Text contains partial word preset and logical product mask to insert in proper field. Not used, reserved for future use. Text contains two words, the first text word is logically shifted left 12 bits. The second text word is modified by the load base plus the address counter physical base address and the result is logically shifted 10 bits to the right. The logical sum of the shifted values produces a 16 bit load word. Text contains two words followed by an externally referenced symbol. One 16 bit load word is produced as in MBS-10. Modify the two text words by the load base plus the address counter physical base address to produce two load words. Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	1 16-bi	t Modify text word by base load value.
value. The text value is not relocated. The symbol is entered in a table for succeeding references. Text contains offset followed by an externally referenced symbol. End of MBS for this image. Text contains a relative address to allocate forward references. Text contains partial word preset and logical product mask to insert in proper field. Not used, reserved for future use. Text contains two words, the first text word is logically shifted left 12 bits. The second text word is modified by the load base plus the address counter physical base address and the result is logically shifted 10 bits to the right. The logical sum of the shifted values produces a 16 bit load word. Text contains two words followed by an externally referenced symbol. One 16 bit load word is produced as in MBS-10. Modify the two text words by the load base plus the address counter physical base address to produce two load words. Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	2	The given text value is relocated and the symbol is entered
symbol. End of MBS for this image. Text contains a relative address to allocate forward references. Text contains partial word preset and logical product mask to insert in proper field. Not used, reserved for future use. Text contains two words, the first text word is logically shifted left 12 bits. The second text word is modified by the load base plus the address counter physical base address and the result is logically shifted 10 bits to the right. The logical sum of the shifted values produces a 16 bit load word. Text contains two words followed by an externally referenced symbol. One 16 bit load word is produced as in MBS-10. Modify the two text words by the load base plus the address counter physical base address to produce two load words. Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	3	value. The text value is not relocated. The symbol is entered
Text contains a relative address to allocate forward references. Text contains partial word preset and logical product mask to insert in proper field. Not used, reserved for future use. Text contains two words, the first text word is logically shifted left 12 bits. The second text word is modified by the load base plus the address counter physical base address and the result is logically shifted 10 bits to the right. The logical sum of the shifted values produces a 16 bit load word. Text contains two words followed by an externally referenced symbol. One 16 bit load word is produced as in MBS-10. Modify the two text words by the load base plus the address counter physical base address to produce two load words. Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	4	
Text contains partial word preset and logical product mask to insert in proper field. 8,9 Not used, reserved for future use. Text contains two words, the first text word is logically shifted left 12 bits. The second text word is modified by the load base plus the address counter physical base address and the result is logically shifted 10 bits to the right. The logical sum of the shifted values produces a 16 bit load word. Text contains two words followed by an externally referenced symbol. One 16 bit load word is produced as in MBS-10. Modify the two text words by the load base plus the address counter physical base address to produce two load words. Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	5	End of MBS for this image.
to insert in proper field. 8,9 Not used, reserved for future use. Text contains two words, the first text word is logically shifted left 12 bits. The second text word is modified by the load base plus the address counter physical base address and the result is logically shifted 10 bits to the right. The logical sum of the shifted values produces a 16 bit load word. Text contains two words followed by an externally referenced symbol. One 16 bit load word is produced as in MBS-10. Modify the two text words by the load base plus the address counter physical base address to produce two load words. Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	6	Text contains a relative address to allocate forward references.
10 Text contains two words, the first text word is logically shifted left 12 bits. The second text word is modified by the load base plus the address counter physical base address and the result is logically shifted 10 bits to the right. The logical sum of the shifted values produces a 16 bit load word. 11 Text contains two words followed by an externally referenced symbol. One 16 bit load word is produced as in MBS-10. 12 Modify the two text words by the load base plus the address counter physical base address to produce two load words. 13 Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	7	
left 12 bits. The second text word is modified by the load base plus the address counter physical base address and the result is logically shifted 10 bits to the right. The logical sum of the shifted values produces a 16 bit load word. 11 Text contains two words followed by an externally referenced symbol. One 16 bit load word is produced as in MBS-10. 12 Modify the two text words by the load base plus the address counter physical base address to produce two load words. 13 Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	8,9	Not used, reserved for future use.
symbol. One 16 bit load word is produced as in MBS-10. Modify the two text words by the load base plus the address counter physical base address to produce two load words. Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	10	left 12 bits. The second text word is modified by the load base plus the address counter physical base address and the result is logically shifted 10 bits to the right. The logical sum of
counter physical base address to produce two load words. Text contains two words followed by an externally referenced symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	11	
symbol. Two load words are produced as in MBS-12. Text - AN/UYK-20(V) machine instructions or data to be allocated to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	12	
Word to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.	13	
		to absolute memory locations. Text for external definitions or references. Instructions and data consist of 16-bit words.

A. External Definition Text Entry (M = 2 or 3)

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
							Va	lue								
Character 1							Character 2									
		Ch	ara	cter	3			Character 4								
Character 5								Character 6								
Character 7								Character 8								

8 ASCII Characters Left Justified Blank Filled

Figure 5. Modification Codes

B. External Reference Text Entry (M = 4)

15 14 13 12 11 10 9 8 Offset		
Character 1	Character 2	8 ASCII
Character 3	Character 4	Characters
Character 5	Character 6	Left Justified
Character 7	Character 8	Blank Filled

C. Partial Word Preset (M=7).

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Mask															
Data															
	Address														

D. Page Register Text Entry (M = 10)

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
					()							M	IP	
	Relative Address														

where: MP-Memory Protection bits

E. External Reference Page Register Text Entry (M=11)

15 14 13 12 11 10	9 8	7	6	5	4	3	2	1	0	
0						M	P			
	Offset	Va	lue							
Character 1		Character 2								
Character 3		Character 4								
Character 5	Character 5									
Character 7			Ch	ara	cter	8				

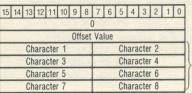
Where: MP-Memory Protection bits 8 ASCII Char. Left-Justified Blank-Filled Symbolic Name

Figure 5. Modification Codes (continued)

Physical Address Text Entry (M = 12)

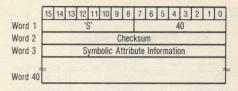


External Reference Physical Address Text Entry (M = 13)

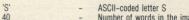


8 ASCII Char. Left-Justified

Symbolic Name



Description of S-Image:



Number of words in the image
The 16-bit two's complement arithmetic sum of all the
words in the image not including the checksum word
CMS-2M symbol table entries for use in high-level Checksum

Word 3-40 debugging

Reference NAVSEA 0967-LP-598-2210, Appendix E.

Figure 5. Modification Codes (continued)

Figure 5a. Symbolic Debugging Image Format

	15 14 13 12 11 10 9 8	7 6 5 4 3 2 1 0	
Word 0	'E'	40	
Word 1	F Zero	Address Counter Number	
Word 2	Relative	Location	
Word 3	Character 1	Character 2	Flement Name
Word 4	Character 3	Character 4	8 ASCII Char.
Word 5	Character 5	Character 6	Left Justified
Word 6	Character 7	Character 8	Blank Filled
Word 7	Character 1	Character 2	System Name
Word 8	Character 3	Character 4	8 ASCII Char.
Word 9	Character 5	Character 6	Left Justified
Word 10	Character 7	Character 8	Blank Filled
Word 11	Number of Sour	ce Card Images	
Word 12	Number of I	Fatal Errors	
Word 13	Number of W	arning Errors	
Word 14	Number of Cau	itionary Errors	
Word 15	Total No. of Images	Output in Element	
Word 16	Month	Month	Month, Day
Word 17	Day	Day	Year 6 ASCII
Word 18	Year	Year	Characters
Word 19	Hour	Hour	Hour, Minute
Word 20	Minutes	Minutes	Second 6 ASC
Word 21	Seconds	Seconds	Characters
Word 22	No. of Source Sta	tements Compiled	
Word 23	No. of Comment St	atements Compiled	
Wrd 24- 39	ZE	RO	

'E' ASCII Coded Letter 'E' 40 Number of words in image Flag Indicator 1 - No entry point to element (CMS-2M SYS-DD's or SYS-PROC with no procedure or function) 0 - Entry point to element is given (CMS-2M SYS-PROC's with a procedure or function) AC Number Address Counter Number of entry point Relative Location Relative Location of element entry point (First procedure or function in SYS-PROC) Element Name Name of element System Name Name of SYSTEM Word 11 Number of Source Card Images in this element Word 12 Number of Fatal Errors in this element Word 13 Number of Warning Errors in this element Number of Cautionary Errors in this element Word 14 Total Number of Images Output for this element Word 15 Word 16-18 Date of last patch to this element Word 19-21 Time of last patch to this element Word 22 Number of Source Statements Compiled Word 23 Number of Comment Statements Compiled Word 24-39 **ZERO**

Figure 6. End Image format